

# STORAGE APPARATUS AND SHIELDING METHOD FOR STORAGE APPARATUS

## CROSS-REFERENCES TO RELATED APPLICATIONS

This application relates to and claims priority from Japanese Patent Application No. 2003-390210, filed on November 20, 2003, the entire disclosure of which is incorporated herein by reference.

## BACKGROUND OF THE INVENTION

### Field of the Invention

The present invention relates to a storage apparatus and a shielding method for a storage apparatus.

### Description of the Related Art

With the recent advancement of information technology, storage apparatuses and information processing devices which exist in connection with storage apparatuses are becoming higher and higher in performance. To cope with increases in performance, the storage apparatuses and the information processing devices have adopted various techniques for preventing electromagnetic waves from leaking to the outside or entering from the outside.

The storage apparatuses are required to have extremely high reliability, and positive interception of electromagnetic waves is particularly important. For this reason, the storage apparatuses adopt various shielding

structures for intercepting electromagnetic waves.

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#### SUMMARY OF THE INVENTION

A multiplicity of electronic component parts are incorporated at high density in a storage apparatus. Therefore, with respect to the storage apparatus, it is strongly desired to realize simplified manufacture, improved maintainability, reduced manufacturing cost and a reduced number of component parts while strengthening interception of electromagnetic waves.

The invention has been made in view of the above-described problem and mainly provides a storage apparatus and a shielding method for a storage apparatus.

The invention provides, therefore, a storage apparatus which includes: a storage control section including a first rack having electrical conductivity, channel control parts housed in the first rack, and disk control parts housed in the first rack, the channel control parts being communicably connected to an information processing apparatus and constructed to receive a data input/output request from the information processing apparatus, the disk control parts being communicably connected to hard disk drives for storing data and constructed to perform read/write of data from and to the hard disk drives in response to a data input/output

request from the information processing apparatus; a storage drive section including a second rack having electrical conductivity, the hard disk drives, and relay parts for relaying communications between the hard disk drives and the disk control parts, the hard disk drives and the relay parts being housed in the second rack; and communication cables for communicably connecting the disk control parts to the relay parts, each of the communication cables including a transmission medium through which to transmit data to be read or written by the disk control parts, a first conductor having electrical conductivity and surrounding the transmission medium with an insulator interposed therebetween, a second conductor having electrical conductivity and surrounding the first conductor with an insulator interposed therebetween, and an electrically nonconductive covering surrounding the second conductor. The first conductor is electrically conductibly connected to ground potential supply circuits provided in at least either the disk control parts or the relay parts, and the second conductor is electrically conductibly connected to at least one of the first rack and the second rack.

Other problems disclosed in the present application as well as methods for solving the problems will become apparent from the following description of embodiments of the invention, taken in conjunction with the accompanying

drawings.

According to the invention, it is possible to provide a storage apparatus and a shielding method for a storage apparatus.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a view showing an external appearance and construction of a storage apparatus according to an embodiment of the invention;

Fig. 2 is a view showing an external appearance and construction of a control section according to the embodiment;

Fig. 3 is a view showing an external appearance of one of drive sections according to the embodiment;

Fig. 4 is a view showing the state in which control boxes are housed in the control section according to the embodiment;

Fig. 5 is a view showing the state in which disk drive boxes are housed in one of the drive sections according to the embodiment;

Fig. 6 is a block diagram showing an internal construction of the storage apparatus according to the embodiment;

Fig. 7 is a block diagram showing the state in which disk adapters and storage volumes are communicably connected to one another according to the embodiment;

Fig. 8 is a view showing a construction of another drive section;

Fig. 9 is a view showing one of the drive sections according to the embodiment;

Fig. 10 is a view showing an external appearance of one example of the connection between disk adapters and hard disk drives according to the embodiment;

Fig. 11 is a view showing an external appearance of another example of the connection between disk adapters and hard disk drives according to the embodiment;

Fig. 12 is a block diagram showing the state of connection between disk adapters and hard disk drives according to the embodiment;

Fig. 13 is a view showing an external appearance the state of connection between disk adapters and hard disk drives according to the embodiment;

Fig. 14 is a view showing a communication cable according to the embodiment;

Fig. 15 is a view showing the state in which Fibre Channel switches according to the embodiment are fitted;

Fig. 16 is a view showing the state in which frame ground potential is supplied to communication cables in the storage apparatus according to the embodiment;

Fig. 17 is a view showing the state in which frame ground potential is supplied to communication cables in the storage

apparatus according to the embodiment;

Fig. 18 is a view showing a communication cable according to another embodiment;

Fig. 19 is a view showing an external appearance of the connection between disk adapters and hard disk drives according to another embodiment;

Fig. 20 is a view showing the state in which a communication cable is routed in a storage device according to another embodiment;

Fig. 21 is a view showing the state in which a communication cable is routed in a storage device according to the embodiment;

Fig. 22 is a view showing the state in which communication cables are connected to Fibre Channel switches according to the embodiment;

Fig. 23 is a view showing the state in which covers are respectively provided on Fibre Channel switches according to the embodiment;

Fig. 24 is a view showing the state in which a communication cable is routed in a storage device according to another embodiment;

Fig. 25 is a view showing the state in which a communication cable is routed in a storage device according to the embodiment;

Fig. 26 is a view showing the state in which a cover is

provided on a control box according to the embodiment;

Fig. 27 is a view showing the state in which rack covers are provided on one of the drive sections according to the embodiment;

Fig. 28 is a view showing an elastic body having electrical conductivity according to the embodiment;

Fig. 29 is a block diagram showing a construction of an channel adapter according to the embodiment;

Fig. 30 is a block diagram showing a construction of a disk adapter according to the embodiment; and

Fig. 31 is a view showing the state in which rack covers are provided on the control section according to the embodiment.

#### DESCRIPTION OF THE SPECIFIC EMBODIMENTS

##### (EXTERNAL APPEARANCE OF DISK ARRAY APPARATUS)

An external appearance and construction of a storage apparatus (hereinafter referred to also as a disk array apparatus) 100 according to one embodiment of the invention will be described below with reference to Fig. 1.

The disk array apparatus 100 shown in Fig. 1 includes a control section (a storage control section) 110 and drive sections (storage drive sections) 120. In the example shown in Fig. 1, the control section 110 is disposed in the middle of the disk array apparatus 100, and drive sections 120 are

disposed on the right and left sides of the control section 110.

The control section 110 is responsible for controlling the entire disk array apparatus 100. As will be described later in detail, logical parts 420 responsible for controlling the entire disk array apparatus 100 and disk drive units 310 for storing data are disposed on the front and rear sides of the control section 110. Disk drive units 310 are disposed on the front and rear sides of the drive sections 120.

The disk array apparatus 100 contains various electronic devices at high density so that its large data storage capacity and a reduction in size can be compatibly realized. The disk array apparatus 100 is also provided with various constructions for intercepting electromagnetic waves from the outside. Detailed constructions of the control section 110 and the drive sections 120 will be described below with reference to Figs. 2 through 5.

#### (CONTROL SECTION)

Figs. 2 and 4 show the construction of the control section 110. Fig. 2 shows a front perspective view of an external appearance of the control section 110, seen from the right direction, as well as a rear perspective view of the external appearance of the control section 110, seen from the left direction. The external appearance shown on the left

side of Fig. 2 corresponds to the front perspective view of the external appearance seen from the right direction, while the external appearance shown on the right side of Fig. 2 corresponds to the rear perspective view of the external appearance seen from the left direction.

The control section 110 has an approximately rectangular parallelepipedic framework (a first rack) 200 having electrical conductivity, in which disk drive modules (disk boxes) 300, logical modules (control boxes) 400, batteries 800, AC-BOXES 700, AC/DC power sources 600, fans 500 and an operator panel 111 are housed. The framework 200 may be made of, for example, a metal having electrical conductivity.

Each of the disk drive modules 300 has an approximately rectangular parallelepipedic shape as well as electrical conductivity. Each of the disk drive modules 300 may be made of, for example, a metal having electrical conductivity. The disk drive modules 300 are housed in the upper portion of the framework 200 so that the disk drive modules 300 are electrically conductibly connected to the framework 200. A plurality of disk drive units 310 are housed in each of the disk drive modules 300 in such a manner that each of the disk drive units 310 is adjacent to the next one and is removably inserted in the disk drive modules 300, and Fibre Channel switches (FSWs or relay parts) 150 are also housed in each

of the disk drive modules 300 so that each of the Fibre Channel switches 150 can be removably inserted in the disk drive modules 300. The disk drive modules 300 and the framework 200 may be arranged to conduct electricity therebetween by being connected to each other by electrical cables, or by being brought into simple physical contact with each other.

Each of the disk drive units 310 is constructed in such a manner that a disk drive (hard disk drive) for storing data is housed in a canister. The FSWs 150 will be described later.

Each of the logical modules 400 has electrical conductivity and an approximately rectangular parallelepipedic shape. The logical modules 400 may be made of, for example, a metal having electrical conductivity. The logical modules 400 are housed in the middle portion of the framework 200 so that the logical modules 400 are electrically conductibly connected to the framework 200. The logical modules 400 and the framework 200 may be arranged to conduct electricity therebetween by being connected to each other by electrical cables, or by being brought into simple physical contact with each other.

Each of the logical modules 400 is provided with a logical part 420 and logical module fans 410. Control boards 430 provided with various functions for controlling read/write of data from and to disk drives 311 are housed in

the logical part 420 so that each of the control boards 430 is removably inserted in the logical part 420. Although details will be described later, each of the circuit boards 430 of the logical part 420 includes channel adapters (channel control parts which are communicably connected to an information processing apparatus 1000 and receive a data input/output request from the information processing apparatus 1000) 131, a cache memory 133, a shared memory 135, a connection part 132, and disk adapters (disk control parts which are communicably connected to hard disk drives for storing data and perform read/write of data from and to the hard disk drives in response to a data input/output request) 134. The logical module fans 410 are devices for producing cooling air to cool the logical part 420. Cooling air enters the inside of the framework 200 from the front side of the logical modules 400 through the gap between each of the circuit boards 430 of the logical parts 420, and is discharged from the ceiling portion of the framework 200 to the outside thereof by being drawn by the logical module fans 410 and fans 500.

The batteries 800, the AC-BOXES 700 and the AC/DC power sources 600 are housed in the lower portion of the framework 200. The batteries 800, the AC-BOXES 700 and the AC/DC power sources 600 are hereinafter referred to also as a power source part.

The AC-BOXES 700 are inlets for introducing alternating current power into the disk array apparatus 100, and function as a breaker. Alternating current power introduced into the AC-BOXES 700 is supplied to the AC/DC power sources 600.

The AC/DC power sources 600 are power source units for converting the introduced alternating current voltage to direct current voltage and outputting direct current voltage to be supplied to the logical parts 420, the disk drive units 310 and the like.

The batteries 800 are power storage units for supplying direct current power to various devices provided in the control section 110 instead of the AC/DC power sources 600 during a failure in the supply of direct current power from the AC/DC power sources 600, such as a power failure or an abnormality occurring in the AC/DC power sources 600.

The fans 500 are disposed on the ceiling portion of the framework 200. The fans 500 are units for producing cooling air to cool the control section 110. Cooling air enters the inside of the framework 200 from the front side of each of the disk drive module 300 and the logical modules 400, and is discharged to the outside of the framework 200 by being drawn by the fans 500.

The operator panel 111 is disposed on the front side of the framework 200. The operator panel 111 is a unit for accepting operation inputs from an operator who performs

maintenance and management on the disk array apparatus 100.

(DRIVE SECTION)

Figs. 3 and 5 show the construction of one of the drive sections 120. Fig. 3 shows a front perspective view of an external appearance of one of the drive sections 120, seen from the right direction.

The drive section 120 shown in Fig. 3 has an approximately rectangular parallelepipedic framework (a second rack) 200 having electrical conductivity, in which disk drive modules (disk boxes) 300, batteries 800, AC-BOXES 700, AC/DC power sources 600 and fans 500 are housed. These devices provided in the drive section 120 are the same as the corresponding devices provided in the control section 110.

It is to be noted that the framework 200 used in the control section 110 and the framework 200 used in each of the drive sections 120 can be constructed with the same structure. In this case, if the logical modules 400 are housed in the middle portion of the framework 200, it is possible to provide the control section 110, whereas if the disk drive modules 300 are housed in the middle portion of the framework 200, it is possible to provide the drive section 120.

(CONSTRUCTION OF DISK ARRAY APPARATUS)

Fig. 6 is a block diagram showing an internal construction of the disk array apparatus 100 according to this embodiment of the invention. The disk array apparatus 100

is communicably connected to the information processing apparatus 1000 via a SAN (Storage Area Network) 900.

The information processing apparatus 1000 is an information apparatus such as a computer having a CPU (Central Processing Unit) and memories. Various functions can be realized by various programs being executed by the CPU provided in the information processing apparatus 1000. The information processing apparatus 1000 can be used as, for example, a main computer in an ATM cash dispenser system for banks or a seat reservation system for airliners.

The SAN 900 is a network for exchange of data between the information processing apparatus 1000 and the disk array apparatus 100. In general, the communication performed between the information processing apparatus 1000 and the disk array apparatus 100 via the SAN 900 obeys the Fibre Channel Protocol. A data input/output request is transmitted from the information processing apparatus 1000 to the disk array apparatus 100 in accordance with the Fibre Channel Protocol.

The disk array apparatus 100 according to the embodiment of the invention is provided with a disk array control part 130 and disk array drive parts 140. The disk array control part 130 is arranged in the control section 110, and the disk array drive parts 140 are arranged in the control section 110 or the drive sections 120. Namely, the control section 110

is provided with the disk array control part 130 and the disk array drive parts 140, and the respective drive sections 120 are provided with the disk array drive parts 140.

The disk array control part 130 receives a data input/output request from the information processing apparatus 1000, and performs read/write of data from and to the disk drives 311 provided in the disk array drive parts 140.

The disk array control part 130 includes the channel adapters 131, the cache memory 133, the connection part 132, the shared memory 135, the disk adapters (hereinafter referred to also as DKFs) 134, and a management terminal (hereinafter referred to also as an SVP) 136. The channel adapters 131, the cache memory 133, the connection part 132, the shared memory 135 and the disk adapters 134 are constructed on the circuit boards 430 which constitute each of the logical parts 420 shown in Fig. 4.

#### (CHANNEL ADAPTER)

The channel adapters 131 are communicably connected to the information processing apparatus 1000, and receives a data input/output request from the information processing apparatus 1000 and performs exchange of data with the information processing apparatus 1000.

The hardware construction of one of the channel adapters 131 is shown in Fig. 29. As shown in Fig. 29, the hardware

of each of the channel adapters 131 is constructed as part of one unit board provided with a circuit board. Each of the channel adapters 131 includes a network interface part 451, a memory 453, an input/output control part 454, an I/O (Input/Output) processor 459, a NVRAM (Non-Volatile RAM) 455, a board-connecting connector 456, and a communication connector 457. Other circuits similar to those of general electronic circuits, such as a voltage supply circuit and a ground potential supply circuit, are provided on the circuit board of each of the channel adapters 131.

The network interface part 451 is provided with a communication interface for establishing communication with the information processing apparatus 1000. For example, the network interface part 451 receives a data input/output request transmitted from the information processing apparatus 1000 in accordance with the Fibre Channel Protocol. The communication connector 457 is a connector to which a cable via which the network interface part 451 communicates with the information processing apparatus 1000 is to be connected. The communication connector 457 supports, for example, the Fibre Channel.

The input/output control part 454 is responsible for controlling the entire channel adapter 131, and performs exchange of data and commands with the disk adapters 134, the cache memory 133, the connection part 132 and the management

terminal 136. The input/output control part 454 executes various programs stored in the memory 453, thereby realizing various functions of the channel adapter 131. The input/output control part 454 is provided with the I/O processor 459 and the NVRAM 455. The I/O processor 459 controls the exchange of data and commands. The NVRAM 455 is a non-volatile memory which stores a program responsible for control of the I/O processor 459. The content of the program which is stored in the NVRAM 455 can be written and rewritten from the management terminal 136.

#### (CACHE MEMORY AND SHARED MEMORY)

The cache memory 133 and the shared memory 135 are memories which store data and commands to be exchanged between the channel adapter 131 and the disk adapters 134. For example, if a data input/output request which the channel adapters 131 has received from the information processing apparatus 1000 is a write request, the channel adapter 131 writes the write request to the shared memory 135, and also writes to the cache memory 133 data received from the information processing apparatus 1000. Then, the corresponding one of the disk adapters 134 reads the data written to the cache memory 133 and writes the read data to the corresponding one of the disk drives 311 in accordance with the write request written to the shared memory 135.

#### (CONNECTION PART)

The connection part 132 provides interconnections among the channel adapters 131, the shared memory 135, the cache memory 133 and the disk adapters 134. The connection part 132 includes, for example, a crossbar switch.

(DISK ADAPTER)

Each of the disk adapters 134 is communicably connected to the corresponding ones of the disk drives 311, and performs read/write of data from and to the corresponding ones of the disk drives 311 by communicating therewith. The read/write of data is performed via a communication path which constitutes a loop determined by FC-AL which is a Fibre Channel Standard (hereinafter referred to also as the FC-AL loop). The communication path includes the disk adapters 134, communication cables 160, the FSWs 150, and the disk drives 311. The communication between each of the disk adapters 134 and the corresponding ones of the disk drives 311 is relayed by the FSW 150 provided in each of the disk array drive parts 140.

The hardware construction of one of the disk adapters 134 is shown in Fig. 30.

As shown in Fig. 30, the hardware of each of the disk adapters 134 is constructed as part of one unit board provided with a circuit board.

Each of the disk adapters 134 includes an interface part 461, a memory 463, an input/output control part 462, a NVRAM

(Non-Volatile RAM) 464, a board-connecting connector 465, and communication connectors 466. Other circuits similar to those of general electronic circuits, such as a source voltage supply circuit and a ground potential supply circuit, are provided on the circuit board of each of the disk adapters 134.

The interface part 461 is provided with a communication interface for establishing communication with the corresponding ones of the disk drives 311. Predetermined ones of the communication cables 160 are connected to the communication connector 466.

The CPU 462 is responsible for controlling the entire disk adapter 134. The CPU 462 executes various programs stored in the memory 463 and the NVRAM 464, thereby realizing various functions of the disk adapter 134.

The NVRAM 464 is a non-volatile memory which stores a program responsible for control of the CPU 462. The content of the program which is stored in the NVRAM 464 can be written and rewritten from the management terminal 136.

#### (MANAGEMENT TERMINAL)

The management terminal 136 is an information processing device for effecting maintenance and management of the disk array apparatus 100. The management terminal 136 may be, for example, a notebook computer provided with a display and a keyboard which are constructed in a foldable

form. The management terminal 136 is housed in the control section 110. Of course, the management terminal 136 may not be housed in the control section 110, and may also be, for example, a remote computer connected to the disk array apparatus 100 via a communications network. In addition, the form of the management terminal 136 is not limited to a notebook computer, and may also be, for example, a desktop computer. Furthermore, the management terminal 136 may be an information processing device for exclusively effecting maintenance and management of the disk array apparatus 100, or may also be a general-purpose information processing device to which a function for effecting maintenance and management of the disk array apparatus 100 is added.

It is to be noted that the channel adapters 131, the disk adapters 134, the cache memory 133, the shared memory 135 and the connection part 132 need not be separately provided, and may also be constructed in an integrated form. In addition, a combination of at least any ones of these circuits may also be constructed in an integrated form.

(FIBRE CHANNEL SWITCH (FSW))

Fig. 7 shows the construction of each of the FSWs 150 and the FC-AL loop which is formed by one of the disk adapters 134, the FSWs 150, the disk drives 311 and the communication cables 160.

The FC-AL loop can be formed by connecting the disk

adapter 134 and the disk drives 311 to multiplexers 151 provided in the FSWs 150. In the example shown in Fig. 7, one FC-AL loop is formed across two FSWs 150.

One of the FSWs 150 is communicably connected to the disk adapter 134 and to the other FSW 150 via the communication cables 160 connected to connectors 153. The disk drives 311 are also communicably connected to each of the FSWs 150. The connections between the disk drives 311 and the FSWs 150 can also be provided by, for example, data transfer paths on a circuit board provided inside each of the drive sections 120. Of course, the disk drives 311 and the FSWs 150 may also be connected via the communication cables 160.

A select signal applied to each of the multiplexers 151 is a signal for selecting either the input to a side indicated by "1" or the input to a side indicated by "0" in each of the multiplexers 151. In the case where the disk adapter 134 or any one of the disk drives 311 is connected to each of the multiplexers 151, the select signal is inputted so that the input to the side indicated by "1" of each of the multiplexers 151 is selected. In the case where there is a multiplexer 151 to which nothing is connected, the select signal is inputted to this multiplexer 151 so that the input to the side indicated by "0" is selected. In addition, if a fault is detected in, for example, a particular one of the disk drives 311, the select signal is inputted to a multiplexer 151 to

which the particular disk drive 311 is connected, so that the input to the side indicated by "0" is selected. Control of the select signal to be inputted to each of the multiplexers 151 is performed by, for example, the corresponding one of control parts 152.

The control parts 152 exert control on the respective FSWs 150. To control the FSWs 150 is to control the select signals to be inputted to, for example, the respective multiplexers 151. The control of the select signals by the control parts 152 is performed, for example, when a particular one of the disk drives 311 is to be set to a state communicable or non-communicable with the disk adapters 134.

Other circuits similar to those of general electronic circuits, such as a voltage supply circuit and a ground potential supply circuit, are provided on each of the FSWs 150.

As shown in Fig. 9, in each of the drive sections 120 according to the embodiment of the invention, the AC/DC power sources 600 are housed in the lower portion of the framework 200, and some of the FSWs 150 are housed between the upper and lower ones of the disk drive units 310 housed in the upper portion of each of the disk drive modules 300, while the other are housed between the upper and lower ones of the disk drive units 310 housed in the lower portion of the same disk drive module 300. On the other hand, in a drive section 1120 of

the type shown in Fig. 8, AC/DC power sources 1600 and FSWs 1150 are housed in the state of being juxtaposed to disk drive units 1310 in each disk drive module 1300. In each of the drive sections 120 according to the embodiment, since the arrangement of the FSWs 150 and the AC/DC power sources 600 is contrived as described above, a lateral width W2 of the drive section 120 can be made smaller than a lateral width W1 of the drive section 1120. Accordingly, it is possible to reduce the overall size of the drive section 120.

(EXTERNAL APPEARANCE OF CONNECTION FROM DISK ADAPTER TO DISK DRIVE)

Figs. 10 to 13 show a routing of the communication cables 160 from the disk adapters 134 provided in the control section 110 to the disk drives 311 provided in the control section 110 or the drive sections 120. Fig. 10 is a view showing an external appearance of a routing of the communication cables 160 in the disk array apparatus 100 having a construction provided with two disk adapters 134. Fig. 11 is a view showing an external appearance of a routing of the communication cables 160 in the disk array apparatus 100 having a construction provided with four disk adapters 134. Fig. 12 is a block diagram showing the connection relationship between the disk adapters 134 and the FSWs 150. Fig. 13 is a block diagram schematically showing a routing of the communication cables 160 from the disk adapters 134 to the

disk drives 311.

As shown in Figs. 10 to 13, the communication cables 160 which communicably interconnect the disk adapters 134, the FSWs 150 and the disk drives 311 are disposed to reach every location in the inside of the disk array apparatus 100.

Accordingly, the disk array apparatus 100 is required to have a structure which does not allow the communication cables 160 to function as an antenna receiving electromagnetic waves radiated from the outside of the disk array apparatus 100.

The communication between the disk adapters 134 and the disk drives 311 is performed in accordance with the Fibre Channel Standard, and the speed of the communication reaches several gigahertz (approximately 1 to 4 gigahertz). In this manner, signals of high frequency flow in the communication cables 160, so that strong electromagnetic waves are radiated from the communication cables 160. Accordingly, the disk array apparatus 100 is required to intercept electromagnetic waves radiated from the communication cables 160.

Strong electromagnetic waves are radiated from not only the communication cables 160 but also the channel adapters 131, the disk adapters 134, the FSWs 150 and the AC/DC power sources 600. Accordingly, there is also a need for a structure which intercepts electromagnetic waves radiated from these circuit devices such as the channel adapters 131 and the FSWs

150.

(COMMUNICATION CABLE)

In the disk array apparatus 100 according to the embodiment of the invention, each of the communication cables 160 has the structure shown in Fig. 14.

The communication cables 160 according to the embodiment of the invention has a structure in which a cable 168 has connectors 161 at its opposite ends. One of the connectors 161 is connected to the communication connectors 466 of any one of the disk adapters 134, while the other is connected to the connector 153 of any one of the FSWs 150. The cable 168 has a transmission medium 162 through which data to be read or written by the connected disk adapter 134 is to be transmitted, a first shield (conductor) 164 which has electrical conductivity and surrounds the transmission medium 162 with an insulator 163 interposed therebetween, a second shield (conductor) 166 which has electrical conductivity and surrounds the first shield 164 with an insulator 165 interposed therebetween, and an electrically nonconductive covering 167 which surrounds the second shield 166.

The transmission medium 162 may be made of, for example, annealed copper wire. The annealed copper wire may be plated with, for example, tin. The insulators 163 and 165 may be made of, for example, vinyl chloride or polyethylene foam.

The first shield 164 may have a structure made of, for example, woven annealed copper wire. The first shield 164 may also be made of, for example, polyester tape on which aluminum foil is stuck. The second shield 166 may have a structure made of, for example, woven annealed copper wire. The second shield 166 may also be made of, for example, polyester tape on which aluminum foil is stuck. The covering 167 may be made of, for example, vinyl chloride.

The first shield 164 is electrically conductibly connected to pins of the respective connectors 161 in the inside of the cable 168. When the communication cable 160 is connected to the communication connectors 466 of one of the disk adapters 134 or to the connectors 153 of one of the FSWs 150, the first shield 164 can be electrically conductibly connected to the ground potential supply circuit provided in at least one of the connected disk adapter 134 and FSW 150. Accordingly, the potential of the first shield 164 can be set to ground potential, whereby interception of electromagnetic waves can be effected.

Fig. 15 shows the state in which the communication cables 160 are respectively connected to the connectors 153 of the FSWs 150. In the embodiment, up to four FSWs 150 can be fitted in one disk drive module 300 as shown in Fig. 15, and as viewed in Fig. 15, two right-hand ones of the FSWs 150 and the other two left-hand ones differ from each other in

fitting height. The FSWs 150 according to the embodiment can be removably fitted into the disk drive modules 300, and the respective FSWs 150 are fitted with levers for fixing or releasing them to or from the disk drive module 300. As shown in Fig. 15, the two right-hand FSWs 150 and the two left-hand FSWs 150 are made different from each other in the height of their fitting positions, whereby the interference between the levers of the two right-hand FSWs 150 and the levers of the two left-hand FSWs 150 is prevented. Accordingly, the distance between the right-hand and left-hand FSWs 150 can be made narrow and the width W2 of each of the drive sections 120 can be made small.

The second shield 166 of the communication cable 160 is communicably connected to the framework 200. The framework 200 to which the second shield 166 is connected is the framework 200 of the control section 110, the framework 200 of each of the drive sections 120, or the frameworks 200 of both sections 110 and 120. The second shield 166 and the framework 200 can be arranged to conduct electricity therebetween, by being connected to each other by an electrical cable or the like, or by being connected to each other by the use of frame ground supply parts (ground potential supply parts) 170 which will be described below.

Figs. 6 and 7 show that the communication cables 160 are connected to ground. As described above, this illustration

shows that the first shield 164 is communicably connected to at least any one of the ground potential supply circuits provided in the disk adapter 134 and the FSW 150, and the second shield 166 is electrically conductibly connected to the framework 200.

(FRAME GROUND SUPPLY PART)

As shown in Figs. 13 and 15, the frame ground supply parts 170 are electrically conductibly fixed to each of the logical modules 400 and the disk drive modules 300. Accordingly, the frame ground supply parts 170 are provided electrically conductibly to at least either the logical modules 400 or the disk drive modules 300.

Fig. 16 shows one of the frame ground supply parts 170. The frame ground supply part 170 includes a first cable clamping member (a first communication cable clamping part) 171 which has electrical conductivity and a first surface 174, a second cable clamping member (a second communication cable clamping part) 172 which has electrical conductivity and is electrically conductibly connected to at least either the framework 200 of the control section 110 or the framework 200 of any of the drive sections 120 and has a second surface 175, and a cable fixing part (a fixing part) 173 which fixes the first cable clamping member 171 and the second cable clamping member 172 so that the members 171 and 172 are pressed against each other with the first surface 174 and the second surface

175 facing each other.

Incidentally, the frame ground supply part 170 shown in Fig. 16 is constructed so that the first cable clamping member 171 and the second cable clamping member 172 are separable from each other, but the first cable clamping member 171 and the second cable clamping member 172 may be joined together by hinges or the like.

A method of electrically conductibly connecting the second shields 166 of the respective communication cables 160 to the framework 200 by the use of the frame ground supply part 170 is as follows. Namely, as shown in Fig. 16, each of the communication cables 160 has a portion from which the covering 167 is removed around its periphery, and the portions of the respective communication cables 160 are held by being clamped between the first surface 174 of the first cable clamping member 171 and the second surface 175 of the second cable clamping member 172.

Accordingly, since the second shields 166 are electrically conductibly connected to the frame ground supply part 170, the second shields 166 can be electrically conductibly connected to the framework 200. In addition, since the second shields 166 are electrically conductibly connected to the framework 200, the potential of each of the second shields 166 can be maintained at ground potential, whereby interception of electromagnetic waves can be realized.

Accordingly, in the disk array apparatus 100 according to the embodiment of the invention, the effect of interception of electromagnetic waves effect by the first shield 164 and the effect of interception of electromagnetic waves by the second shield 166 are mutually potentiated, whereby data to be read and written from and to the disk drives 311 can be strongly protected from external electromagnetic waves. At the same time, it is possible to prevent outward leaks of electromagnetic waves generated by communication between the disk adapters 134 and the disk drives 311. Furthermore, the electrical connection between the second shields 166 and the framework 200 can be provided merely by clamping the portions of the communication cables 160 from which the respective coverings 167 are removed, by the use of the frame ground supply part 170, whereby the work of routing the communication cables 160 becomes easy. Accordingly, it is possible to facilitate the manufacture and maintenance of the disk array apparatus 100 and reduce manufacturing cost. In addition, since the communication cables 160 are clamped by the frame ground supply part 170, it is possible to positively provide electrical connection between the second shields 166 and the framework 200. Accordingly, it is possible to improve the reliability of the disk array apparatus 100.

As shown in Fig. 16, in the frame ground supply part 170 according to the embodiment of the invention, hollow portions

176 each having a shape similar to part of the peripheral shape of the second shield 166 are formed in at least one of the first surface 174 and the second surface 175. This construction makes it possible to increase the area of contact between each of the second shields 166 and the frame ground supply part 170, an electric conductivity is improved between the second shield 166 and the framework 200 whereby the effect of interception of electromagnetic waves interception by the second shield 166 can be made far stronger.

It is to be noted that the frame ground supply part 170 can also have the structure shown in Fig. 17. Namely, the second surface 175 of the second cable clamping member 172 does not have the hollow portions 176, but the hollow portions 176 are formed on the first surface 174 of the first cable clamping member 171. This structure makes it possible to facilitate manufacture of the frame ground supply parts 170 and reduce manufacturing cost.

Accordingly, in the disk array apparatus 100 according to the embodiment of the invention, it is possible to positively effect interception of electromagnetic waves within each of the communication cables 160, whereby there is no need to provide a special structure for interception of electromagnetic waves. For example, in a disk array apparatus using communication cables 1160 of the type shown in Fig. 18, there is a need for a special structure for

interception of electromagnetic waves as shown in Figs. 19 and 20.

The communication cable 1160 shown in Fig. 18 has a structure in which a cable 1168 has connectors 1161 at its opposite ends. One of the connectors 1161 is connected to a communication connector of any one of disk adapters 1134, while the other is connected to a connector of any one of FSWs 1150. The cable 1168 has a transmission medium 1162 through which data to be read or written by the connected disk adapter 1134 is to be transmitted, a first shield 1164 which has electrical conductivity and surrounds the transmission medium 1162 with an insulator 1163 interposed therebetween, and an electrically nonconductive covering 1167 which surrounds the first shield 1164.

The first shield 1164 is electrically conductibly connected to pins of the respective connectors 1161 in the inside of the cable 1168. When the communication cable 1160 is connected to connectors of one of the disk adapters 1134 or to connectors of one of the FSWs 1150, the first shield 1164 is electrically conductibly connected to the ground potential supply circuit provided in at least one of the connected disk adapter 1134 and FSW 1150. Accordingly, the potential of the first shield 1164 is maintained at ground potential, whereby interception of electromagnetic waves can be effected.

However, the communication cable 1160 shown in Fig. 18 is not able to intercept electromagnetic waves passing through the first shield 1164. Accordingly, in the control section 1100 and the drive sections 1200 each of which uses the communication cables 1160 of the type shown Fig. 18, the respective communication cables 1160 need to be covered with communication cable holding parts 1180, as shown in Fig. 19 by way of example. Figs. 20 and 24 show the manner in which one communication cable 1160 is covered with one communication cable holding part 1180.

The communication cable holding part 1180 has a structure in which a communication cable housing part 1181 is covered with an electromagnetic-wave intercepting cover 1182. Furthermore, electromagnetic-wave intercepting sheets 1190 are fitted between the communication cable housing part 1181 and the electromagnetic-wave intercepting cover 1182 in order to prevent electromagnetic waves from leaking or entering through a gap between the communication cable housing part 1181 and the electromagnetic-wave intercepting cover 1182. Each of the communication cable housing part 1181 and the electromagnetic-wave intercepting cover 1182 may be manufactured of a material such as an electrically conductive metal. The electromagnetic-wave intercepting sheet 1190 may be formed of an elastic material having an electromagnetic-wave absorbing effect, such as urethane.

Accordingly, it is possible to intercept electromagnetic waves by covering the communication cable 1160 with the communication cable holding part 1180.

However, in the inside of the disk array apparatus in which electronic devices are disposed at high density and the communication cables 1160 are disposed to reach every location as shown in Figs. 10 and 11, it is not necessarily preferable to route the communication cables 1160 covered with the communication cable holding parts 1180, in terms of ease of manufacture as well as maintainability.

In the inside of the disk array apparatus in particular, if the direction of any of the communication cables 1160 is to be changed, the communication cable 1160 needs to be sharply bent as shown in Fig. 24, because the number of kinds of electromagnetic-wave intercepting covers 1182 cannot be increased in terms of a reduction in the number of component parts.

On the other hand, in the disk array apparatus 100 according to the embodiment of the invention, since interception of electromagnetic waves can be positively effected within each of the communication cables 160, there is no need to incorporate the above-described special structure for interception of electromagnetic waves.

For example, a communication cable holding part 180 according to the embodiment of the invention can have the

structure shown in Fig. 21. Namely, there is no need to use the electromagnetic-wave intercepting cover 1182 and the electromagnetic-wave intercepting sheets 1190. Accordingly, the communication cable 160 can be easily put into and taken out of the communication cable holding part 180, whereby the routing workability of the communication cables 160 is improved. Accordingly, with respect to the disk array apparatus 100, it is possible to realize simplified manufacture, improved maintainability, reduced manufacturing cost and a reduced number of component parts. Furthermore, it is also possible to reduce the overall size of the disk array apparatus 100.

The communication cable holding part 180 according to the embodiment of the invention does not need the electromagnetic-wave intercepting cover 1182 nor the electromagnetic-wave intercepting sheets 1190. Accordingly, in the case where the communication cable holding part 180 is formed as shown in Fig. 25 by way of example, the communication cable 1160 can be gently bent if the direction of any of the communication cables 1160 needs to be changed in the inside of the disk array apparatus 100. Accordingly, it is possible to realize not only prevention of damage to the communication cables 160 but also restraint of an increase in the electrical resistance of the transmission medium 162, whereby it is also possible to improve the quality of

communication signals flowing in the communication cables 160. Because communication speeds specified in the Fibre Channel Standard are high speeds, the restraint of an increase in the electrical resistance of the transmission medium 162 and an improvement in signal quality are extremely important in terms of improvements in the performance and reliability of the disk array apparatus 100.

#### (SHIELDING OF OTHER PARTS)

As described previously, in the disk array apparatus 100 according to the embodiment of the invention, only the communication cables 160 do not generate electromagnetic waves. Electromagnetic waves are also generated from the channel adapters 131, the FSWs 150, the AC/DC power sources 600 and the like. Accordingly, there is also a need for a structure for intercepting electromagnetic waves radiated from the channel adapters 131, the FSWs 150, the AC/DC power sources 600 and the like.

#### (SHIELDING OF FSW)

Figs. 22 and 23 show a structure for intercepting electromagnetic waves generated from the FSWs 150.

Specifically, one face of the disk drive module 300 through which the FSWs 150 are to be inserted and removed is provided with Fibre Channel switch covers 320 each of which includes a conductive plate having electrical conductivity and approximately hermetically seals the area of the face of

the disk drive module 300 through which the FSWs 150 are to be inserted and removed. Each of the Fibre Channel switch covers 320 may be manufactured of a material such as an electrically conductive metal. In addition, each of the Fibre Channel switch covers 320 may also be provided with an electromagnetic-wave restraining sheet (elastic body) 190 having electrical conductivity, which is disposed to extend along the periphery of the face of the Fibre Channel switch cover 320 that is opposite to the face of the disk drive module 300 through which the FSWs 150 are to be inserted and removed.

The electromagnetic-wave restraining sheet 190 is formed by covering with a nylon coat 192 an elastic body having an electromagnetic-wave absorbing effect, such as an urethane foam 191. An external appearance of the electromagnetic-wave restraining sheet 190 is shown in Fig. 28. This construction makes it possible to prevent electromagnetic waves generated from the FSWs 150 from leaking to the outside. At the same time, it is possible to prevent malfunctions or the like of the FSWs 150 from being caused by electromagnetic waves outside the disk array apparatus 100. In addition, it is possible to prevent far more positively leakage and penetration of electromagnetic waves by sealing the gaps between the Fibre Channel switch covers 320 and the FSWs 150 by means of the electromagnetic-wave restraining sheets 190.

(SHIELDING OF LOGICAL MODULE)

Fig. 26 shows a structure for intercepting electromagnetic waves generated from the logical modules 400.

Specifically, the faces of the respective logical modules 400 through which the channel adapters 131 and the disk adapters 134 are to be inserted and removed are provided with logical module covers 440 each of which includes a conductive plate having electrical conductivity and approximately hermetically seals the face of a respective one of the logical modules 400 through which the channel adapters 131 and the disk adapters 134 are to be inserted and removed. Each of the logical module covers 440 may be manufactured of a material such as an electrically conductive metal. In addition, each of the logical module covers 440 may also be provided with electromagnetic-wave restraining sheets (elastic bodies) 190 having electrical conductivity, which are arranged to surround the periphery of the face of each of the logical module covers 440 that is opposite to the face of the corresponding logical module 400 through which the channel adapters 131 and the disk adapters 134 are to be inserted and removed.

Each of the electromagnetic-wave restraining sheets 190 is formed by covering with the nylon coat 192 an elastic body having an electromagnetic-wave absorbing effect, such as the urethane foam 191. This construction makes it possible to prevent electromagnetic waves generated from the disk

adapters 134, the channel adapters 131 and the like from leaking to the outside of the disk array apparatus 100. At the same time, it is possible to prevent malfunctions or the like of the channel adapters 131 and the disk adapters 134 from being caused by electromagnetic waves outside the disk array apparatus 100. In addition, it is possible to prevent far more positively leakage and penetration of electromagnetic waves by sealing the gaps between the logical module covers 440 and the logical modules 400 by means of the electromagnetic-wave restraining sheets 190.

(SHIELDING OF CONTROL SECTION AND DRIVE SECTION)

Fig. 31 shows a structure for intercepting electromagnetic waves generated from the control section 110. Fig. 27 shows a structure for intercepting electromagnetic waves generated from each of the drive sections 120. The control section 110 and the drive sections 120 emit electromagnetic waves generated by, for example, the AC/DC power sources 600. There is also a case where small quantities of electromagnetic waves leak from the communication cables 160.

Each of the frameworks 200 of the control section 110 and the drive sections 120 has at least four faces respectively provided with framework panels 210 which respectively include conductive plates having electrical conductivity, and each of the frameworks 200 of the control

section 110 and the drive sections 120 is approximately hermetically sealed by the framework panels 210.

Each of the framework panels 210 may be manufactured of a material such as an electrically conductive metal plate. In addition, each of the framework panels 210 may also be provided with electromagnetic-wave restraining sheets (elastic bodies) 190 having electrical conductivity, and the electromagnetic-wave restraining sheets 190 are disposed to surround the periphery of each of the framework panels 210 that are respectively opposed to respective faces of the framework 200 of any one of the control section 110 and the drive sections 120. This construction makes it possible to prevent electromagnetic waves generated from the AC/DC power sources 600, the communication cables 160 and the like from leaking to the outside of the disk array apparatus 100. At the same time, it is possible to prevent electromagnetic waves outside the disk array apparatus 100 from causing malfunctions or the like of the AC/DC power sources 600 and from affecting data input/output signals flowing in the communication cables 160. In addition, it is possible to prevent far more positively leakage and penetration of electromagnetic waves by sealing the gaps between the framework panels 210 and each of the frameworks 200 by means of the electromagnetic-wave restraining sheets 190.

As described hereinabove, in the disk array apparatus

100 according to the embodiment of the invention, electromagnetic waves generated from the transmission media 162 of the respective communication cables 160 by communications between the disk adapters 134 and the disk drives 311 can be prevented from leaking to the outside of the communication cables 160. Accordingly, it is possible to prevent leakage of electromagnetic waves from the disk array apparatus 100. In addition, it is also possible to prevent electromagnetic waves emitted from electronic devices outside the disk array apparatus 100 from entering the inside of any of the communication cables 160. Accordingly, it is possible to improve the reliability of communications between the disk adapters 134 and the disk drives 311.

In addition, since interception of electromagnetic waves is effected between the inside and the outside of each of the communication cables 160, the frameworks 200 of the disk array apparatus 100 do not need to have a special structure for intercepting electromagnetic waves. Furthermore, since there is no leakage of electromagnetic waves from the communication cables 160, a structure for intercepting electromagnetic waves leaking from the communication cables 160 does not need to be provided in the disk array apparatus 100. Accordingly, it is possible to increase the degree of freedom of routing of the communication

cables 160 in the disk array apparatus 100. Accordingly, in the disk array apparatus 100 according to the embodiment of the invention, it is possible to realize simplified manufacture, simplified maintenance, reduced manufacturing cost and a reduced number of component parts while strengthening the interception of electromagnetic waves.

Furthermore, in the disk array apparatus 100 according to the embodiment of the invention, the Fibre Channel switch covers 320 are respectively provided on the portions of the disk drive modules 300 in which the FSWs 150 are housed. In addition, the faces of the respective logical modules 400 through which the channel adapters 131 and the disk adapters 134 are to be inserted and removed are provided with the logical module covers 440 each of which includes a conductive plate having electrical conductivity and approximately hermetically seals the face of a respective one of the logical modules 400 through which the channel adapters 131 and the disk adapters 134 are to be inserted and removed. In addition, each of the frameworks 200 of the control section 110 and the drive sections 120 has at least four faces respectively provided with the framework panels 210 which respectively include conductive plates having electrical conductivity, and each of the frameworks 200 of the control section 110 and the drive sections 120 are approximately hermetically sealed by the framework panels 210. In this manner, the disk array

apparatus 100 according to the embodiment of the invention is capable of intercepting electromagnetic waves far more strongly and positively.

While preferred embodiments of the invention have been described hereinabove, the above-described embodiments have been referred to for ease of understanding of the invention, but is not to be construed as limiting the invention. To the contrary, the invention can be variously modified and improved without departing from its spirit and scope, and encompasses all equivalents.